

? show files

File 347:JAPIO Dec 1976-2009/Nov(Updated 100228)
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File 350:Derwent WPIX 1963-2010/UD=201019
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Set	Items	Description
S1	400551	{ADDRESS? ? OR {WEB OR FTP OR WWW OR HTTP}{1W}SERVER? ? OR NAME? ?(1N)SERVER? ? OR DNS OR {IP OR INTERNET()PROTOCOL}{1}ADDRESS?? OR {HOST OR DOMAIN}{1}NAME? ? OR SERVER(){ID OR IDENTIFICATION})
S2	78544	S1(3N){DETERMIN??? OR DEFIN??? OR SET OR ESTABLISH??? OR SELECT??? OR LIMIT? ? OR LIMITATION? ? OR DECID??? OR CHECK? OR TEST? OR EXAMIN? OR ANALYZ? OR ANALYS? OR ASSESS? OR IDENTIF? OR SCRUTINIZ? OR SCRUTINIS? OR EVALUAT????}
S3	19645	{POSITION? ? OR LOCATION? ? OR OFFSET OR OFF(){SET}{3N}{FIELD? ? OR DATA(3W){STRUCTURE? ? SCHEMA? ? OR MODEL? ?}}
S4	85	S2(5N)S3
S5	1578	{INDEX?? OR INDICE? ?}{3N}ARRAY? ?
S6	17	S2(5N)S5
S7	13	S6 AND PY=1963:2003
S8	4	S7 NOT MEMOR?
S9	30	S4 NOT MEMOR?
S10	20	S9 AND PY=1963:2003
S11	20	S10 NOT RF

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Subject summary

? t/ 3,k/ all

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8/3,K/1 (Item 1 from file: 350)

DIALOG(R) File 350: Derwent WPIX

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0013204451 *Drawing available*

WPI Acc no: 2003-288705/**200328**

Related WPI Acc No: 2003-266732; 2003-299215; 2004-535289; 2004-570245; 2004-579380; 2005-403762; 2005-457312; 2006-018388

XRPX Acc No: N2003-229536

Branch target address cache in pipelined microprocessor, operates parallel with instruction cache by receiving fetch address which is used to select target address as subsequent fetch address

Patent Assignee: INTELLIGENCE FIRST CO (INTE-N); IP FIRST LLC (IPFI-N)

Inventor: HENRY G G; MCDONALD T C; HENRY G; MACDONALD T

Patent Family (4 patents, 3 countries)							
Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
US 20020194461	A1	20021219	US 2001849736	A	20010504	200328	B
CN 1397886	A	20030219	CN 2002118548	A	20020427	200337	E
TW 535109	A	20030601	TW 2001132642	A	20011228	200374	E
CN 1217271	C	20050831	CN 2002118548	A	20020427	200647	E

Priority Applications (no., kind, date): US 2001849736 A 20010504

Patent Details					
Patent Number	Kind	Lan	Pgs	Draw	Filing Notes
US 20020194461	A1	EN	43	19	
TW 535109	A	ZH			

Original Publication Data by AuthorityArgentina**Publication No.** ... **Claims:**executed branch instructions;an input, coupled to said array, for receiving the fetch address, to **index** into said **array** of storage elements to **select** one of **said** target **addresses**; andan **output**, coupled to said array, for providing said one of said target addresses indexed by the... ... Basic Derwent Week: **200328**...

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8/3,K/2 (Item 2 from file: 350)

DIALOG(R) File 350: Derwent WPIX

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0012326595 *Drawing available*

WPI Acc no: 2002-268441/**200231**

XRPX Acc No: N2002-208904

Generating connection identification for use on network in reusable manner so that risk of misrouting any data packets relating to a destroyed connection can be significantly reduced

Patent Assignee: NOKIA CORP (OYNO); NOKIA NETWORKS OY (OYNO)

Inventor: HIPPELAINEN L; SAIRANEN J; HIPPELAINEN L

Patent Family (9 patents, 92 countries)							
Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
WO 2001093540	A1	20011206	WO 2000EP5011	A	20000531	200231	B
AU 200050743	A	20011211	AU 200050743	A	20000531	200231	E
			WO 2000EP5011	A	20000531		
EP 1290858	A1	20030312	EP 2000935160	A	20000531	200320	E
			WO 2000EP5011	A	20000531		
JP 2003535507	W	20031125	WO 2000EP5011	A	20000531	200380	E
			JP 2001588205	A	20000531		
JP 3805255	B2	20060802	WO 2000EP5011	A	20000531	200652	E
			JP 2001588205	A	20000531		
US 7289504	B1	20071030	WO 2000EP5011	A	20000531	200772	E
			US 2003296478	A	20030514		
CA 2413879	C	20080212	CA 2413879	A	20000531	200813	E
			WO 2000EP5011	A	20000531		
EP 1290858	B1	20090422	EP 2000935160	A	20000531	200929	E
			WO 2000EP5011	A	20000531		
DE 60042084	E	20090604	DE 60042084	A	20000531	200936	E
			EP 2000935160	A	20000531		
			WO 2000EP5011	A	20000531		

Priority Applications (no., kind, date): WO 2000EP5011 A 20000531

Patent Details						
Patent Number	Kind	Lan	Pgs	Draw	Filing Notes	
WO 2001093540	A1	EN	38	5		
National Designated States,Original	AE AG AL AM AT AU AZ BA BB BG BR BY CA CH CN CR CU CZ DE DK DM DZ EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT TZ UA UG US UZ VN YU ZA ZW					
Regional Designated States,Original	AT BE CH CY DE DK EA ES FI FR GB GH GM GR IE IT KE LS LU MC MW MZ NL OA PT SD SE SL SZ TZ UG ZW					
AU 200050743	A	EN			PCT Application	WO 2000EP5011
					Based on OPI patent	WO 2001093540
EP 1290858	A1	EN			PCT Application	WO 2000EP5011
					Based on OPI patent	WO 2001093540
Regional Designated States,Original	AL AT BE CH CY DE DK ES FI FR GB GR IE IT LI LT LU LV MC MK NL PT RO SE SI					
JP 2003535507	W	JA	41		PCT Application	WO 2000EP5011
					Based on OPI patent	WO 2001093540
JP 3805255	B2	JA	18		PCT Application	WO 2000EP5011
					Previously issued patent	JP 2003535507
					Based on OPI patent	WO 2001093540
US 7289504	B1	EN			PCT Application	WO 2000EP5011
					Based on OPI patent	WO 2001093540
CA 2413879	C	EN			PCT Application	WO 2000EP5011
					Based on OPI patent	WO 2001093540
EP 1290858	B1	EN			PCT Application	WO 2000EP5011
					Based on OPI patent	WO 2001093540
Regional Designated States,Original	AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU MC NL PT SE					
DE 60042084	E	DE			Application	EP 2000935160
					PCT Application	WO 2000EP5011
					Based on OPI patent	EP 1290858
					Based on OPI patent	WO 2001093540

Original Publication Data by AuthorityArgentina**Publication No. ...****Claims:**a new identification when said allocated identifier is reallocated to a new connection, wherein said **identification** is an IPv6 **address**, and said allocated **identifier** is an **index** to a context **array** (25), wherein said reuse information is stored in said context array (25), and wherein said... .. a new identification when said allocated identifier is reallocated to a new connection, wherein said **identification** is an IPv6 **address**, and said allocated **identifier** is an **index** to a context **array**, wherein said reuse information is stored in said context array, and wherein said reuse information... .. Basic Derwent Week: **200231**...

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8/3,K/3 (Item 3 from file: 350)

DIALOG(R)File 350: Derwent WPIX

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0008745014 *Drawing available*

WPI Acc no: 1998-287183/**199825**

XRPX Acc No: N1998-225668

Data cache for performing store cycles in single clock cycle - speculatively stores data within

predicted way of cache after capturing data currently being stored in same predicted way

Patent Assignee: ADVANCED MICRO DEVICES INC (ADMI)

Inventor: HATTANGADI R M; WITT D B

Patent Family (5 patents, 85 countries)							
Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
WO 1998020420	A1	19980514	WO 1996US17517	A	19961104	199825	B
AU 199712708	A	19980529	WO 1996US17517	A	19961104	199841	E
			AU 199712708	A	19961104		
EP 1015980	A1	20000705	EP 1996943477	A	19961104	200035	E
			WO 1996US17517	A	19961104		
EP 1015980	B1	20020424	EP 1996943477	A	19961104	200228	E
			WO 1996US17517	A	19961104		
DE 69620920	E	20020529	DE 69620920	A	19961104	200243	E
			EP 1996943477	A	19961104		
			WO 1996US17517	A	19961104		

Priority Applications (no., kind, date): WO 1996US17517 A 19961104

Patent Details						
Patent Number	Kind	Lan	Pgs	Draw	Filing Notes	
WO 1998020420	A1	EN	26	4		
National Designated States,Original	AL AM AT AU AZ BA BB BG BR BY CA CH CN CU CZ DE DK EE ES FI GB GE HU IL IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK TJ TM TR TT UA UG UZ VN					
Regional Designated States,Original	AT BE CH DE DK EA ES FI FR GB GR IE IT KE LS LU MC MW NL OA PT SD SE SZ UG					
AU 199712708	A	EN			PCT Application	WO 1996US17517
					Based on OPI patent	WO 1998020420
EP 1015980	A1	EN			PCT Application	WO 1996US17517
					Based on OPI patent	WO 1998020420
Regional Designated States,Original	DE ES GB					
EP 1015980	B1	EN			PCT Application	WO 1996US17517
					Based on OPI patent	WO 1998020420
Regional Designated States,Original	DE ES GB					
DE 69620920	E	DE			Application	EP 1996943477
					PCT Application	WO 1996US17517
					Based on OPI patent	EP 1015980
					Based on OPI patent	WO 1998020420

Original Publication Data by AuthorityArgentina**Publication No.** ...**Claims:**port, to receive at last one input address and associated input data, wherein said input **address selects** in **indexed** storage locations in the data array from each of which array output data is provided; a n-way... array (250) configured to receive said input address and to store addresses associated with data **stored in said data array**; a tag comparison block (262) coupled to said cache tag array, wherein said tag comparison... Basic Derwent Week: **199825**...

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8/3,K/4 (Item 4 from file: 350)

DIALOG(R)File 350: Derwent WPIX

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0008488365 *Drawing available*

WPI Acc no: 1998-017953/**199802**

Related WPI Acc No: 1996-401968; 1997-350591; 1998-427430

XRPX Acc No: N1998-013756

Master-slave cache system for access by central processing units execution pipeline - includes tag pipeline which has number of stages for simultaneously processing number of requests by passing requests through successive stages of tag pipeline in response to clock

Patent Assignee: EXPONENTIAL TECHNOLOGY INC (EXPO-N)

Inventor: COHEN E T; PATTIN J C

Patent Family (1 patents, 1 countries)							
Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
US 5692152	A	19971125	US 1994267658	A	19940629	199802	B
			US 1996649115	A	19960514		

Priority Applications (no., kind, date): US 1994267658 A 19940629; US 1996649115 A 19960514

Patent Details						
Patent Number	Kind	Lan	Pgs	Draw	Filing Notes	
US 5692152	A	EN	19	7	C-I-P of application	US 1994267658
					C-I-P of patent	US 5551001

Original Publication Data by AuthorityArgentina**Publication No.** ...**Claims:**valid indicator for each element for each index-portion of the address, the tag RAM **array** including slave-valid **indicators** for **identifying** elements in the tag RAM array which also have valid data in the slave cache... Basic Derwent Week: **199802**

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11/3,K/1 (Item 1 from file: 347)

DIALOG(R)File 347: JAPIO

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01920813 **Image available**

MAGNETORESISTANCE TYPE THIN FILM HEAD

Pub. No.: 61-134913 [JP 61134913 A]

Published: June 23, 1986 (**19860623**)

Inventor: NAGATA YUJI

FUKAZAWA TOSHIO

Applicant: MATSUSHITA ELECTRIC IND CO LTD [000582] (A Japanese Company or Corporation) , JP (Japan)

Application No.: 59-256722 [JP 84256722]

Filed: December 05, 1984 (19841205)

Journal: Section: P, Section No. 514, Vol. 10, No. 330, Pg. 53, November 11, 1986 (19861111) ...

Published: **19860623**)

ABSTRACT

...in the figure. The reproducing sensitivity and the linear responsiveness are most excellent in this **set** bias **position**. an **address** magnetic **field** flowed from a magnetic recording medium passes a front yoke 20 and a rear yoke... Di01

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11/3,K/2 (Item 1 from file: 350)

DIALOG(R)File 350: Derwent WPIX

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0012469051 *Drawing available*

WPI Acc no: 2002-415399/**200244**

XRPX Acc No: N2002-326774

Transmitting control information between control unit and sub unit, involves generating information message of predetermined length at message generator including CU identifier value and control information

Patent Assignee: ERICSSON INC (TELF); TELEFONAKTIEBOLAGET ERICSSON L M (TELF)

Inventor: GROHN O I

Patent Family (3 patents, 94 countries)							
Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
WO 2002013410	A2	20020214	WO 2001US23523	A	20010726	200244	B
AU 200180807	A	20020218	AU 200180807	A	20010726	200244	E
US 6925072	B1	20050802	US 2000631474	A	20000803	200550	E

Priority Applications (no., kind, date): US 2000631474 A 20000803

Patent Details						
Patent Number	Kind	Lan	Pgs	Draw	Filing Notes	
WO 2002013410	A2	EN	44	8		
National Designated States,Original	AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT TZ UA UG US UZ VN YU ZA ZW					
Regional Designated States,Original	AT BE CH CY DE DK EA ES FI FR GB GH GM GR IE IT KE LS LU MC MW MZ NL OA PT SD SE SL SZ TR TZ UG ZW					
AU 200180807	A	EN			Based on OPI patent	WO 2002013410

Original Publication Data by AuthorityArgentina**Publication No. ...Claims:**frames, said HDLC frame including an address field having a Service Access Point Identifier (SAPI) **location** for transmitting **said CU identifier**; and placing said CU identifier in said SAPI location.... Basic Derwent Week: **200244...**

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11/3,K/3 (Item 2 from file: 350)

DIALOG(R)File 350: Derwent WPIX

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0012295372 *Drawing available*

WPI Acc no: 2002-236482/**200229**

XRPX Acc No: N2002-181801

ATM (Asynchronous Transfer Mode) switch with logical multicasting for computer networks, a block of consecutive control Index (CIs) are directed to a lookup table which provides a corresponding logical multicast offset

Patent Assignee: GEN DYNAMICS GOVERNMENT SYSTEMS CORP (GEND)

Inventor: KING D R; KRUMPOCH M A; LANE S E; STRUBECK W D

Patent Family (1 patents, 1 countries)							
Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
US 6320858	B1	20011120	US 199882350	A	19980520	200229	B

Priority Applications (no., kind, date): US 199882350 A 19980520

Patent Details					
Patent Number	Kind	Lan	Pgs	Draw	Filing Notes
US 6320858	B1	EN	7	7	

Original Publication Data by AuthorityArgentina**Publication No. ...Claims:**a corresponding predetermined Logical Multicast Offset field ("LMO") which defines the offset from the base **address of the CI** to the next leaf of the

logical multicast; a zero value of the offset indicative that... .. Basic Derwent Week: **200229**...

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11/3,K/4 (Item 3 from file: 350)

DIALOG(R)File 350: Derwent WPIX

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0011214862 *Drawing available*

WPI Acc no: 2002-153893/**200220**

Related WPI Acc No: 2000-012915; 2003-014749; 2004-130424; 2004-550802; 2005-381447; 2005-401613; 2005-646686; 2006-788172; 2007-089099; 2007-394904; 2007-410595; 2008-B38052; 2008-B38761; 2008-C97562; 2008-E20339; 2005-745732; 2006-016768; 2006-209780; 2008-K90012

XPX Acc No: N2002-117010

Network resource requisition method in distributed or multiprocessor environment, involves processing valid domain name as registration request when network resource cannot be located

Patent Assignee: SCHNEIDER E (SCHN-I)

Inventor: SCHNEIDER E

Patent Family (1 patents, 1 countries)							
Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
US 6338082	B1	20020108	US 1999125531	P	19990322	200220	B
			US 1999130136	P	19990420		
			US 1999157075	P	19991001		
			US 1999160125	P	19991018		
			US 2000175825	P	20000113		
			US 2000525350	A	20000315		

Priority Applications (no., kind, date): US 1999125531 P 19990322; US 1999130136 P 19990420; US 1999157075 P 19991001; US 1999160125 P 19991018; US 2000175825 P 20000113; US 2000525350 A 20000315

Patent Details						
Patent Number	Kind	Lan	Pgs	Draw	Filing Notes	
US 6338082	B1	EN	18	5	Related to Provisional	US 1999125531
					Related to Provisional	US 1999130136
					Related to Provisional	US 1999157075
					Related to Provisional	US 1999160125
					Related to Provisional	US 2000175825

Alerting Abstract ... registration services, hence allows for extended utility of the command line of a device or location field of client browser, for **determining domain name** availability to streamline registration process. Facilitates beginners to more readily understand the difference between resource... .. Basic Derwent Week: **200220**...

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11/3,K/5 (Item 4 from file: 350)

DIALOG(R)File 350: Derwent WPIX

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0010986497 *Drawing available*

WPI Acc no: 2001-611189/**200170**

Related WPI Acc No: 2001-581974; 2001-589779; 2001-607226; 2001-607227; 2001-607228; 2001-611188; 2001-611190; 2001-625673; 2001-625674; 2001-625675; 2002-055065; 2002-055066

XPX Acc No: N2001-456255

Electronic recording system for transaction with security function uses electronic reading device for generating electronic reproduction of written information by detecting positions of electronic reading device relative to address pattern

Patent Assignee: TELEFONAKTIEBOLAGET ERICSSON L M (TELF)
 Inventor: HOLLSTROEM M; JOHANSSON O; OLSSON P; RYDBECK N; TILLGREN M

Patent Family (6 patents, 92 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
WO 2001061630	A1	20010823	WO 2001EP1402	A	20010209	200170	B
AU 200135459	A	20010827	AU 200135459	A	20010209	200176	E
EP 1269405	A1	20030102	EP 2001907514	A	20010209	200310	E
			WO 2001EP1402	A	20010209		
EP 1269405	B1	20040428	EP 2001907514	A	20010209	200429	E
			WO 2001EP1402	A	20010209		
DE 60103044	E	20040603	DE 60103044	A	20010209	200436	E
			EP 2001907514	A	20010209		
			WO 2001EP1402	A	20010209		
US 6952497	B1	20051004	US 2000182742	P	20000216	200565	E
			US 2000190343	P	20000316		
			US 2000192662	P	20000328		
			US 2000703492	A	20001031		

Priority Applications (no., kind, date): US 2000182742 P 20000216; US 2000190343 P 20000316; US 2000192662 P 20000328; US 2000703492 A 20001031

Patent Details

Patent Number	Kind	Lan	Pgs	Draw	Filing Notes	
WO 2001061630	A1	EN	54	12		
National Designated States, Original	AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CR CU CZ DE DK DM DZ EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT TZ UA UG US UZ VN YU ZA ZW					
Regional Designated States, Original	AT BE CH CY DE DK EA ES FI FR GB GH GM GR IE IT KE LS LU MC MW MZ NL OA PT SD SE SL SZ TR TZ UG ZW					
AU 200135459	A	EN			Based on OPI patent	WO 2001061630
EP 1269405	A1	EN			PCT Application	WO 2001EP1402
					Based on OPI patent	WO 2001061630
Regional Designated States, Original	AL AT BE CH CY DE DK ES FI FR GB GR IE IT LI LT LU LV MC MK NL PT RO SE SI TR					
EP 1269405	B1	EN			PCT Application	WO 2001EP1402
					Based on OPI patent	WO 2001061630
Regional Designated States, Original	AL AT BE CH CY DE DK ES FI FR GB GR IE IT LI LT LU LV MC MK NL PT RO SE SI TR					
DE 60103044	E	DE			Application	EP 2001907514
					PCT Application	WO 2001EP1402
					Based on OPI patent	EP 1269405
					Based on OPI patent	WO 2001061630
US 6952497	B1	EN			Related to Provisional	US 2000182742
					Related to Provisional	US 2000190343
					Related to Provisional	US 2000192662

Original Publication Data by AuthorityArgentina**Publication No. ...Claims:**reading device relative to the address pattern as information is written in the data entry **field**, wherein a unique **position** on the **address** pattern is **identified** from an examination of a portion of the address pattern, and wherein the positions of... Basic Derwent Week: **200170...**

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11/3,K/6 (Item 5 from file: 350)

DIALOG(R)File 350: Derwent WPIX

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0009765284 *Drawing available*

WPI Acc no: 2000-052132/**200004**

Related WPI Acc No: 1999-301546; 2000-037233; 2000-222140; 2001-482097; 2002-187908

XRPX Acc No: N2000-040657

Change-detection web server in internet for detecting numerically significant changes in web pages

Patent Assignee: NETMIND TECHNOLOGIES INC (NETM-N)

Inventor: FREIVALD M P; NOBLE A C; RICHARDS M S

Patent Family (1 patents, 1 countries)							
Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
US 5983268	A	19991109	US 1997783625	A	19970114	200004	B
			US 1997823761	A	19970325		

Priority Applications (no., kind, date): US 1997783625 A 19970114; US 1997823761 A 19970325

Patent Details						
Patent Number	Kind	Lan	Pgs	Draw	Filing Notes	
US 5983268	A	EN	16	7	C-I-P of application US 1997783625	

Original Publication Data by AuthorityArgentina**Publication No. ...Claims:**the responder fetching the web page from the remote web-page server and generating markers **for identifying locations** of numeric data **fields** within **the** web page;a **spreadsheet** user-interface, coupled to the responder, for displaying a spreadsheet on the remote client, the... Basic Derwent Week: **200004...**

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11/3,K/7 (Item 6 from file: 350)

DIALOG(R)File 350: Derwent WPIX

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0009728108 *Drawing available*

WPI Acc no: 2000-012928/**200001**

XRPX Acc No: N2000-010040

Program context restoring method for information handling system

Patent Assignee: INT BUSINESS MACHINES CORP (IBMC)

Inventor: AULT D F; PLAMBECK K E; SCALZI C A

Patent Family (1 patents, 1 countries)							
Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
US 5987495	A	19991116	US 1997966374	A	19971107	200001	B

Priority Applications (no., kind, date): US 1997966374 A 19971107

Patent Details						
Patent Number	Kind	Lan	Pgs	Draw	Filing Notes	
US 5987495	A	EN	14	8	Original Publication Data by AuthorityArgentina Publication No. ...Original	

Abstracts:new Resume Program (RP) instruction. The RP instruction contains an operand field specifying through the **selected** register the base **address** of the save **area** together with **offset fields** specifying the **offsets of** the saved contents of the PSW and selected register relative to the beginning of the... Basic Derwent Week: 200001...

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11/3,K/8 (Item 7 from file: 350)

DIALOG(R)File 350: Derwent WPIX

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0009011902 *Drawing available*

WPI Acc no: 1998-568152/**199848**

XRPX Acc No: N1998-442040

Parse tree construction method for source code segment identification in computer system - involves selecting particular value from group containing main node address and end position, and is stored in variant field of record

Patent Assignee: US WEST TECHNOLOGIES INC (USWT-N)

Inventor: ZHU J

Patent Family (1 patents, 1 countries)							
Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
US 5822592	A	19981013	US 1994268088	A	19940628	199848	B
			US 1997791668	A	19970130		

Priority Applications (no., kind, date): US 1994268088 A 19940628; US 1997791668 A 19970130

Patent Details						
Patent Number	Kind	Lan	Pgs	Draw	Filing Notes	
US 5822592	A	EN	8	5	Continuation of application	US 1994268088

Original Publication Data by AuthorityArgentina**Publication No.** ...**Claims:**selected from a group consisting of a parent node address and an end position; and **determining** an end position **based** on the variant **field** of the **first** node record. Basic Derwent Week: **199848**

Dialog eLink: [Order File History](#)

11/3,K/9 (Item 8 from file: 350)

DIALOG(R)File 350: Derwent WPIX

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0007650465 *Drawing available*

WPI Acc no: 1996-270315/**199628**

XRPX Acc No: N1996-227171

User addressing method for controlled access television system - defining collective address word at transmitter including number of bits in word and set position in word larger than bit field and including zeroes

Patent Assignee: KONINK PHILIPS ELECTRONICS NV (PHIG); PHILIPS ELECTRONICS NV (PHIG); PHILIPS ELECTRONIQUE GRAND PUBLIC (PHIG); PHILIPS GLOEILAMPENFAB NV (PHIG); US PHILIPS CORP (PHIG)

Inventor: KERJEAN J; KERJEAN J M; LE B J; LE BERRE J

Patent Family (6 patents, 9 countries)							
Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
EP 716546	A1	19960612	EP 1995203281	A	19951129	199628	B
JP 8237631	A	19960913	JP 1995315353	A	19951204	199647	E
CN 1132448	A	19961002	CN 1995121313	A	19951207	199802	E
US 5912971	A	19990615	US 1995567251	A	19951205	199930	E
EP 716546	B1	19990825	EP 1995203281	A	19951129	199939	E
DE 69511665	E	19990930	DE 69511665	A	19951129	199946	E
			EP 1995203281	A	19951129		

Priority Applications (no., kind, date): FR 199414728 A 19941207

Priority Applications (No. Kind, Date): FR 15041720 A, 15041201

Patent Details						
Patent Number	Kind	Lan	Pgs	Draw	Filing Notes	
EP 716546	A1	FR	5	3		
Regional Designated States,Original		DE ES FR GB IT SE				
JP 8237631	A	JA	4			
EP 716546	B1	FR				
Regional Designated States,Original		DE ES FR GB IT SE				
DE 69511665	E	DE			Application	EP 1995203281
					Based on OPI patent	EP 716546

Original Publication Data by AuthorityArgentina**Publication No.** ...**Claims:**by a field of bits having a length which is equal to the length of **one** of **said** sections, and - a compulsory **position** corresponding to **one** of said sections in thecollective address word, - a collective address word is transmitted by the... ... Basic Derwent Week: **199628**...

Dialog eLink: [Order File History](#)

11/3,K/10 (Item 9 from file: 350)

DIALOG(R) File 350: Derwent WPIX

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0007565773 *Drawing available*

WPI Acc no: 1996-181263/**199619**

XRPX Acc No: N1996-152302

Accessing method for data stored within data cache array - by using two separate content addressable fields for effective address and real address to resolve offset and alias problems

Patent Assignee: INT BUSINESS MACHINES CORP (IBMC)

Inventor: BRANSON B D; LIU P P

Patent Family (3 patents, 3 countries)							
Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
EP 706132	A2	19960410	EP 1995480130	A	19950908	199619	B
EP 706132	A3	19960522	EP 1995480130	A	19950908	199632	E
US 5890221	A	19990330	US 1994319201	A	19941005	199920	E

Priority Applications (no., kind, date): US 1994319201 A 19941005

Patent Details						
Patent Number	Kind	Lan	Pgs	Draw	Filing Notes	
EP 706132	A2	EN	20	10		
Regional Designated States, Original		DE FR GB				
EP 706132	A3	EN				

Original Publication Data by AuthorityArgentina**Publication No.** ...**Original Abstracts:**no match was found within the first content addressable field during the previous cycle. An **offset** condition is **identified** by comparing the translated real **address** to the **content** of the second content addressable field in a cache line when a match has occurred... ... Basic Derwent Week: **199619**...

Dialog eLink: [Order File History](#)

11/3,K/11 (Item 10 from file: 350)

DIALOG(R) File 350: Derwent WPIX

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0007149616 *Drawing available*

WPI Acc no: 1995-185467/199524

XRPX Acc No: N1995-145251

Split line access circuit for high speed cache - has access circuit driving first lower bytes for straight line access and second lower bytes for split line access

Patent Assignee: INTEL CORP (ITLC)

Inventor: GROCHOWSKI E T

Patent Family (1 patents, 1 countries)							
Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
US 5414824	A	19950509	US 199387637	A	19930630	199524	B

Priority Applications (no., kind, date): US 199387637 A 19930630

Patent Details					
Patent Number	Kind	Lan	Pgs	Draw	Filing Notes
US 5414824	A	EN	24	12	

Alerting Abstract ...The circuit includes a physical address circuit, supplying a physical **address** including a **set field** and a byte **offset field**. The **offset field** has a LAH bit indicative of one of a split line access and a straight... Original Publication Data by AuthorityArgentina**Publication No.** ...**Claims:**a second, adjacent cache line, comprising: (a) a physical address circuit for supplying a physical **address** including a **set field** and a byte **offset field including** a LAH bit indicative of one of a split line access and a straight line access... .. Basic Derwent Week: **199524**...

Dialog eLink: [Order File History](#)

11/3,K/12 (Item 11 from file: 350)

DIALOG(R)File 350: Derwent WPIX

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0007131001 *Drawing available*

WPI Acc no: 1995-163860/199522

XRPX Acc No: N1995-128548

Transmission method for audio visual system bidirectional bus - transmitting frame having device address and data fields and inserting information in data field discriminating commands from requests

Patent Assignee: MATSUSHITA DENKI SANGYO KK (MATU); MATSUSHITA ELEC IND CO LTD (MATU);

MATSUSHITA ELECTRIC IND CO LTD (MATU); SONY CORP (SONY)

Inventor: IMAURA T; IWAMOTO K; KOTABE N; KUSAGAYA Y; MAKI S; OSAKABE Y; SOMA Y; SOUMA Y;

YAMAZAKI H

Patent Family (7 patents, 3 countries)							
Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
EP 651588	A2	19950503	EP 1994307924	A	19941027	199522	B
JP 7131866	A	19950519	JP 1993294182	A	19931030	199529	E
EP 651588	A3	19960605	EP 1994307924	A	19941027	199632	E
US 5666363	A	19970909	US 1994329251	A	19941026	199742	E
EP 651588	B1	19990310	EP 1994307924	A	19941027	199914	E
DE 69416931	E	19990415	DE 69416931	A	19941027	199921	E
			EP 1994307924	A	19941027		
JP 3090827	B2	20000925	JP 1993294182	A	19931030	200051	E

Priority Applications (no., kind, date): JP 1993294182 A 19931030

Patent Details						
Patent Number	Kind	Lang	Pgs	Draw	Filing Notes	
EP 651588	A2	EN	22	12		
Regional Designated States,Original	DE FR GB					
JP 7131866	A	JA	16	11		
EP 651588	A3	EN				
US 5666363	A	EN	20	11		
EP 651588	B1	EN				
Regional Designated States,Original	DE FR GB					
DE 69416931	E	DE			Application	EP 1994307924
					Based on OPI patent	EP 651588
JP 3090827	B2	JA	16		Previously issued patent	JP 07131866

Original Publication Data by AuthorityArgentina**Publication No. ... Claims:**and inserting discriminating information (HDOPR, b6, b5, b4) for discrimination between said command and the **request at a pre-set position** in said data field, and transmitting over said bidirectional bus to other devices the transmission signals having said discrimination... .. Basic Derwent Week: **199522**...

Dialog eLink: [Order File History](#)

11/3,K/13 (Item 12 from file: 350)

DIALOG(R)File 350: Derwent WPIX

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0006831174 *Drawing available*

WPI Acc no: 1994-219575/**199427**

XRPX Acc No: N1994-173554

Branch history prediction for processor - using branch history table with predictive field for each fetch cycle in multiple instruction access accessed by partial instruction address for updating

Patent Assignee: INT BUSINESS MACHINES CORP (IBMC)

Inventor: LEVITAN D S

Patent Family (4 patents, 3 countries)							
Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
EP 605876	A1	19940713	EP 1993120944	A	19931227	199427	B
US 5367703	A	19941122	US 19932289	A	19930108	199501	E
EP 605876	B1	19981021	EP 1993120944	A	19931227	199846	E
DE 69321698	E	19981126	DE 69321698	A	19931227	199902	E
			EP 1993120944	A	19931227		

Priority Applications (no., kind, date): US 19932289 A 19930108

Patent Details						
Patent Number	Kind	Lan	Pgs	Draw	Filing Notes	
EP 605876	A1	EN	11	5		
Regional Designated States,Original	DE FR GB					
US 5367703	A	EN	10	5		
EP 605876	B1	EN				
Regional Designated States,Original	DE FR GB					
DE 69321698	E	DE			Application	EP 1993120944
					Based on OPI patent	EP 605876

Original Publication Data by AuthorityArgentina**Publication No.** ...**Original Abstracts:**address, such as the low order address bits. A particular predictive field within the group is then **selected** which corresponds to the **position** of the branch instruction within the instruction fetch. The content of the selected predictive field... ..
Basic Derwent Week: **199427**...

Dialog eLink: [Order File History](#)

11/3,K/14 (Item 13 from file: 350)

DIALOG(R)File 350: Derwent WPIX

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0006292543 *Drawing available*

WPI Acc no: 1993-086263/**199311**

XRPX Acc No: N1993-065976

Maintaining virtual connection upon partial failure of connection path - using second packet communications network to provide replacement connection path between subscriber exchanges

Patent Assignee: SIEMENS AG (SIEI)

Inventor: MUELLER B; SITTER W

Patent Family (4 patents, 3 countries)							
Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
DE 4128939	A1	19930311	DE 4128939	A	19910830	199311	B
CA 2077127	A	19930301	CA 2077127	A	19920828	199320	E
US 5311502	A	19940510	US 1992936637	A	19920828	199418	E
DE 4128939	C2	19951207	DE 4128939	A	19910830	199602	E

Priority Applications (no., kind, date): DE 4128939 A 19910830

Patent Details					
Patent Number	Kind	Lan	Pgs	Draw	Filing Notes
DE 4128939	A1	DE	7	1	
CA 2077127	A	EN			
US 5311502	A	EN	10	2	
DE 4128939	C2	DE	7	1	

Original Publication Data by AuthorityArgentina**Publication No.** ...**Claims:**in the address field of the call request at least after the appertaining network interface **location** for the **virtual connection** now to be set up to in the **address field** in the transmitted call request and entering address information indicating the called subscriber equipment participating... .. addressed field of the call request that indicates the network interface location for the reentry **into** the first **packet** switching network, a **connecting** path to the appertaining network **interface location** in the second packet switching network and conducting the call request thereto; interchanging, on the... ..
... Basic Derwent Week: **199311**...

Dialog eLink: [Order File History](#)

11/3,K/15 (Item 14 from file: 350)

DIALOG(R)File 350: Derwent WPIX

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0006284312 *Drawing available*

WPI Acc no: 1993-077650/**199310**

XRPX Acc No: N1993-059572

Installing virtual connection in packet exchange network - using call packet permitting construction of virtual connection fed to original exchange position and modified where suitable transmission path not present

Patent Assignee: SIEMENS AG (SIEI)

Inventor: MUELLER B; SITTER W

Patent Family (4 patents, 3 countries)							
Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
DE 4128938	A1	19930304	DE 4128938	A	19910830	199310	B
CA 2077112	A	19930301	CA 2077112	A	19920828	199320	E
US 5307344	A	19940426	US 1992936636	A	19920828	199416	E
DE 4128938	C2	19951214	DE 4128938	A	19910830	199603	E

Priority Applications (no., kind, date): DE 4128938 A 19910830

Patent Details					
Patent Number	Kind	Lang	Pgs	Draw	Filing Notes
DE 4128938	A1	DE	6	1	
CA 2077112	A	EN			
US 5307344	A	EN	10	2	
DE 4128938	C2	DE	6	1	

Original Publication Data by AuthorityArgentinaPublication No. ...**Claims:**field of the modified call request with one another at the first network interface location; **defining** a connecting **path** to the second network interface **location** within the second packet switching network based on the address information now contained in the... ...
Basic Derwent Week: **199310**...

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11/3,K/16 (Item 15 from file: 350)

DIALOG(R)File 350: Derwent WPIX

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0005765184

WPI Acc no: 1991-087451/**199112**

XRPX Acc No: N1991-067567

Efficient pager message receiving method - allowing communication of message signal containing several messages for reception by several paging receivers

Patent Assignee: MOTOROLA INC (MOTI)

Inventor: DELUCA M J; JASINSKI L; SCHWENDEMA R J; SCHWENDEMAN R J; SNOWDEN G; SNOWDEN G O; SNOWDEN G O

Patent Family (11 patents, 18 countries)							
Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
WO 1991003037	A	19910307	WO 1990US3876	A	19900713	199112	B
EP 489034	A1	19920610	EP 1990911548	A	19900713	199224	E
			WO 1990US3876	A	19900713		
US 5128665	A	19920707	US 1989396189	A	19890821	199230	E
			US 1991701339	A	19910510		
JP 4507334	W	19921217	JP 1990511020	A	19900713	199305	E
			WO 1990US3876	A	19900713		
EP 489034	A4	19930804	WO 1990US4577	A	19900814	199527	E
CA 2060513	C	19951010	CA 2060513	A	19900713	199548	E
SG 42975	A1	19971017	SG 19961645	A	19900713	199751	E
EP 489034	B1	19980128	EP 1990911548	A	19900713	199809	E
			WO 1990US3876	A	19900713		
DE 69032011	E	19980305	DE 69032011	A	19900713	199815	E
			EP 1990911548	A	19900713		
			WO 1990US3876	A	19900713		
ES 2111539	T3	19980316	EP 1990911548	A	19900713	199817	E
KR 199514424	B1	19951127	WO 1990US3876	A	19900713	199903	E
			KR 1992700389	A	19920221		

Priority Applications (no., kind, date): US 1989396189 A 19890821; US 1991701339 A 19910510

Patent Details							
Patent Number	Kind	Lan	Pgs	Draw	Filing Notes		
WO 1991003037	A	EN					
National Designated States,Original	CA FI JP KR NO						
Regional Designated States,Original	AT BE CH DE DK ES FR GB IT LU NL SE						
EP 489034	A1	EN	48		PCT Application	WO 1990US3876	
					Based on OPI patent	WO 1991003037	
Regional Designated States,Original	AT BE CH DE DK ES FR GB IT LI LU NL SE						
US 5128665	A	EN	17	9	Continuation of application	US 1989396189	
JP 4507334	W	JA			PCT Application	WO 1990US3876	
					Based on OPI patent	WO 1991003037	
EP 489034	A4	EN					
CA 2060513	C	EN					
SG 42975	A1	EN					
EP 489034	B1	EN	18		PCT Application	WO 1990US3876	
					Based on OPI patent	WO 1991003037	
Regional Designated States,Original	AT BE CH DE DK ES FR GB IT LI LU NL SE						
DE 69032011	E	DE			Application	EP 1990911548	
					PCT Application	WO 1990US3876	
					Based on OPI patent	EP 489034	
					Based on OPI patent	WO 1991003037	
ES 2111539	T3	ES			Application	EP 1990911548	
					Based on OPI patent	EP 489034	
KR 199514424	B1	KO			PCT Application	WO 1990US3876	

Original Publication Data by AuthorityArgentina**Publication No.** ...**Claims:**a) determining the presence of an address signal within the address field matching a predetermined **address** assigned to the **selective call receiver**;
(b) determining the **position** of a data packet associated with the position of the address signal matching the predetermined... Basic Derwent Week: **199112**...

Dialog eLink: [Order File History](#)

11/3,K/17 (Item 16 from file: 350)

DIALOG(R) File 350: Derwent WPIX

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0004372886 *Drawing available*

WPI Acc no: 1988-106969/**198816**

Data processor with implied domain addressing - has instruction decoded to generate control code including branch signal and domain access control signal

Patent Assignee: AMDAHL CORP (AMDA)

Inventor: ANDOH J C; KRAKAUER A S; MAIER R M; TOBIAS R J; ZMYSLOWSKI A J

Patent Family (11 patents, 14 countries)							
Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
EP 264216	A	19880420	EP 1987308809	A	19871005	198816	B
AU 198779713	A	19880421				198824	E
NO 198704135	A	19880509				198824	E
DK 198705379	A	19880415				198827	E
US 4785392	A	19881115	US 1986918491	A	19861014	198848	E
CA 1271262	A	19900703				199032	E
NO 174943	B	19940425	NO 19874135	A	19871001	199420	E
EP 264216	B1	19941130	EP 1987308809	A	19871005	199501	E
DE 3750806	G	19950112	DE 3750806	A	19871005	199507	E
			EP 1987308809	A	19871005		
KR 199409097	B1	19940929	KR 198711353	A	19871013	199635	E
DK 171728	B	19970414	DK 19875379	A	19871014	199722	E

Priority Applications (no., kind, date): US 1986918491 A 19861014

Priority Applications (no kind, date): US 156616451 A 19881014

Patent Details						
Patent Number	Kind	Lan	Pgs	Draw	Filing Notes	
EP 264216	A	EN	28	7		
Regional Designated States,Original		CH DE ES FR GB IT LI NL				
US 4785392	A	EN	22			
CA 1271262	A	EN				
NO 174943	B	NO			Previously issued patent	NO 8704135
EP 264216	B1	EN	23	7		
Regional Designated States,Original		CH DE ES FR GB IT LI NL				
DE 3750806	G	DE			Application	EP 1987308809
					Based on OPI patent	EP 264216
DK 171728	B	DA			Previously issued patent	DK 8705379

Alerting Abstract ...One field identifies a location for address information for an operand. The instruction is decoded to generate a control code including a... Original Publication Data by AuthorityArgentina**Publication No.** ...**Original Abstracts:**of instructions is partitioned into a plurality of fields. At least one field of the instruction register identifies a location for address information for an operand used in execution of the instruction. The instruction is decoded to generate a control... **Claims:**One field identifies a location for address information

for an operand. The instruction is decoded to generate a control code including a... Basic Derwent Week:
198816...

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11/3,K/18 (Item 17 from file: 350)

DIALOG(R) File 350: Derwent WPIX

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0004372885 *Drawing available*

WPI Acc no: 1988-106968/198816

Data processor providing fast entry to emulation - has field of instruction register identifying location for address information for operand used in execution of instruction

Patent Assignee: AMDAHL CORP (AMDA)

Inventor: ANDOH J C; ANDOTH J C; KRAKAUER A S; MAIER R M; TOBIAS R J; ZMYSLOWSKI A J

Patent Family (4 patents, 7 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
EP 264215	A	19880420	EP 1987308808	A	19871005	198816	B
AU 198779714	A	19880421				198824	E
CA 1291271	C	19911022				199149	E
US 5210832	A	19930511	US 1986918483	A	19861014	199320	E
			US 1988245978	A	19880919		
			US 1991725905	A	19910703		

Priority Applications (no., kind, date): US 1986918483 A 19861014; US 1988245978 A 19880919; US 1991725905 A 19910703

Patent Details

Patent Number	Kind	Lan	Pgs	Draw	Filing Notes	
EP 264215	A	EN	26	7		
Regional Designated States,Original	DE FR GB IT					
CA 1291271	C	EN				
US 5210832	A	EN	20	7	Continuation of application	US 1986918483
					Continuation of application	US 1988245978

Original Publication Data by AuthorityArgentina**Publication No.** ...**Original Abstracts:**of instructions is partitioned into a plurality of fields. At least one field of the **instruction** register **identifies a location for address** information for **an** operand used in execution of the instruction. The instruction is decoded to generate a control... Basic Derwent Week: 198816...

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11/3,K/19 (Item 18 from file: 350)

DIALOG(R) File 350: Derwent WPIX

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0004127951

WPI Acc no: 1987-235430/198733

Trapped microaddresses requueing in pipelined data processing system - asserting logic signal bit in tag field position when microinstruction address is selected from decoder

Patent Assignee: NIPPON DIGITAL EQUIP KK (DIGI)

Inventor: CLARK D W; CLARK W D; MISHRA N; MISHRA S N

Patent Family (8 patents, 17 countries)							
Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
WO 1987004820	A	19870813	WO 1987US179	A	19870129	198733	B
AU 198769322	A	19870825				198745	E
CN 1987102174	A	19870909				198842	E
EP 290466	A	19881117	EP 1987901237	A	19870129	198846	E
JP 63503333	W	19881202	JP 1987507035	A	19871029	198903	E
CA 1277039	C	19901127				199102	E
EP 290466	B1	19920930	EP 1987901237	A	19870129	199240	E
			WO 1987US179	A	19870129		
DE 3782047	G	19921105	DE 3782047	A	19870129	199246	E
			EP 1987901237	A	19870129		
			WO 1987US179	A	19870129		

Priority Applications (no., kind, date): US 1986823777 A 19860129

Patent Details							
Patent Number	Kind	Lan	Pgs	Draw	Filing Notes		
WO 1987004820	A	EN	28	8			
National Designated States,Original	AU BR JP KR						
Regional Designated States,Original	AT BE CH DE FR GB IT LU NL SE						
EP 290466	A	EN					
Regional Designated States,Original	CH DE FR GB IT LI NL						
CA 1277039	C	EN					
EP 290466	B1	EN	15	8	PCT Application	WO 1987US179	
					Based on OPI patent	WO 1987004820	
Regional Designated States,Original	CH DE FR GB IT LI NL						
DE 3782047	G	DE			Application	EP 1987901237	
					PCT Application	WO 1987US179	
					Based on OPI patent	EP 290466	
					Based on OPI patent	WO 1987004820	

... asserting logic signal bit in tag field position when microinstruction address is selected from decoder Original Publication Data by AuthorityArgentinaPublication No. ...Original Abstracts:each address is a tag field. The logic signal bit is asserted in the tag field position when a microinstruction address is selected from the decoder. When the trap return microinstruction is executed, on completion of the trap... each address is a tag field. The logic signal bit is asserted in the tag field position when a microinstruction address is selected from the decoder. When the trap return microinstruction is executed, on completion of the trap... Basic Derwent Week: 198733...

Dialog eLink: [Order File History](#)

11/3,K/20 (Item 19 from file: 350)

DIALOG(R)File 350: Derwent WPIX

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0000995411

WPI Acc no: 1975-L0817W/197541

Line representation on display screen - involves unblanking image points arranged in raster and line approach by straight-line elements

Patent Assignee: SIEMENS AG (SIEI)

Patent Family (1 patents, 1 countries)							
Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
DE 2413680	A	19751002	DE 2413680	A	19740321	197541	B
			DE 2413680	A	19740321		

Priority Applications (no., kind, date): DE 2413680 A 19740321

Alerting Abstract ...identified by addresses, and each containing a straight-line element. Each straight-line element is **identified** by the **address** of the associated vector **field** and its **position** in the vector **field**. The data, which identify the position are passed to a vector generator, which supplies the... Basic Derwent Week: **197541**

?

